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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Shunpei Yamazaki, et al. Art Unit : 1765
Serial No. : 09/892,225 Examiner : Mathew J. Song
Filed : June 25, 2001
Title : SEMICONDUCTOR DEVICE AND FABRICATION METHOD THEREFOR

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Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

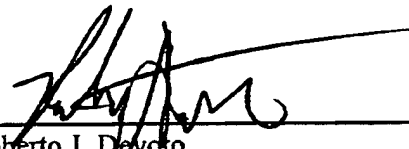
SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT

Supplemental to an Information Disclosure Statement filed on November 22, 2004,
Applicants submit a partial translation of Desig. ID "AO". This partial translation was
inadvertently omitted from the original filing. A copy of the reference listed on the attached
form PTO-1449 is enclosed along with the partial translation.

Please apply any relevant charges or credits to Deposit Account No. 06-1050.

Respectfully submitted,

Date: 8/2/05



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